- · Can Be Used as a 4-Bit Digital Comparator
- Input Clamping Diodes Simplify System Design
- Fully Compatible with Most TTL Circuits

FUNCTION TABLE

INP	UTS	ОUТРUТ
Α	В	Y
L	L	н
L	Н	L
н	L.	L
н	Н	н

H = high level, L = low level

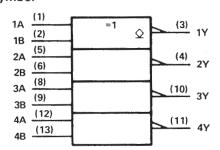
description

The 'LS266 is comprised of four independent 2-input exclusive-NOR gates with open-collector outputs. The open-collector outputs permit tying outputs together for multiple-bit comparisons.

logic symbol (each gate)



logic symbol[†]



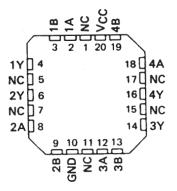
positive logic: $Y = \overline{A \oplus B} = AB + \overline{AB}$

Pin numbers shown are for D, J, N, and W packages.

SN54LS266 . . . J OR W PACKAGE SN74LS266 . . . D OR N PACKAGE (TOP VIEW)

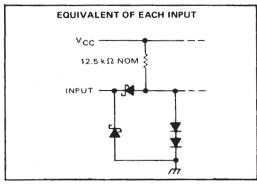
1A 1 14 VCC
1B 2 13 4B
1Y 3 12 4A
2Y 4 11 4Y
2A 5 10 3Y
2B 6 9 3B
GND 7 8 3A

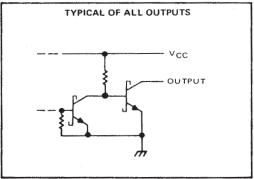
SN54LS266 ... FK PACKAGE (TOP VIEW)



NC - No internal connection

schematic of inputs and outputs







[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

SN54LS266, SN74LS266 QUADRUPLE 2-INPUT EXCLUSIVE-NOR GATES WITH OPEN-COLLECTOR OUTPUTS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1) .						 								7 ١	V
Input voltage														7 ٧	V
Operating free-air temperature range:	SN54LS266		٠.								-5	5°C	c to	125°	С
	SN74LS266											0°	C t	o 70°	С
Storage temperature range															

NOTE 1: Voltage values are with respect to network ground terminal.

recommended operating conditions

	SI	N54LS2	66	SI	UNIT		
	MIN	NOM	MAX	MIN	NOM	MAX	ONT
Supply voltage, V _{CC}	4.5	5	5.5	4.75	5	5.25	٧
High-level output voltage, VOH			5.5			5.5	٧
Low-level output current, IOL			4			8	mA
Operating free-air temperature, T _A	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER			SI	N54LS2	66	S	UNIT			
		TEST CON	MIN	IIN TYP# MAX			TYP‡	MAX	ONT	
VIH	High-level input voltage			2			2			٧
VIL	Low-level input voltage					0.7			0.8	V
VIK	Input clamp voltage	V _{CC} = MIN,	I _I = -18 mA			1.5			-1.5	٧
ЮН	High-level output current	V _{CC} = MIN, V _{IL} = V _{IL} max,	V _{IH} = 2 V, V _{OH} = 5.5 V			100			100	μА
VOL	Low-level output voltage	V _{CC} ≈ MIN, V _{IH} = 2 V,	IOL = 4 mA		0.25	0.4		0.25	0.4	V
VOL		VIL = VIL max	IOL = 8 mA					0.35	0.5	
- la	Input current at maximum input voltage	V _{CC} = MAX,	V1 = 7 V			0.2			0.2	mA
Чн	High-level input current	V _{CC} = MAX,	V ₁ = 2.7 V			40			40	μА
IL	Low-level input current	V _{CC} = MAX,	V _I = 0.4 V			-0.8			-0.8	mA
1cc	Supply current	V _{CC} = MAX,	See Note 2		8	13		8	13	mA

¹ For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type. ‡ All typical values are at V_{CC} = 5 V, T_A = 25 C.

switching characteristics, VCC = 5 V, TA = 25°C

PARAMETER§	FROM (INPUT)	TEST COI	MIN	TYP	MAX	UNIT		
^t PLH	A or B	Other input low	$C_L = 15 \mathrm{pF},$ $R_L = 2 \mathrm{k}\Omega,$ See Note 3		18	30	ns	
tPHL	1	Other siput low				18	30	113
^t PLH	A or B	Other input high				18	30	ns
tpHL	1	Other input high	00011010		18	30		

[§]tpLH = propagation delay time, low-to-high-level output



NOTE 2: 1_{CC} is measured with one input of each gate at 4.5 V, the other inputs grounded, and the outputs open

tpHL = propagation delay time, high-to-low-level output

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.